CORRELATOR 32 ∞ ° IQ DEMO-DULATING CIRCUIT PHASE SHIFTER 53 **5**8 <u> 2</u>6 CONTROLLER QUADRATURE DEMODULATOR FIG. 33-RANSMITT F STAGE <u>,</u> 35 DUPLEXER 1,3

FIG.2

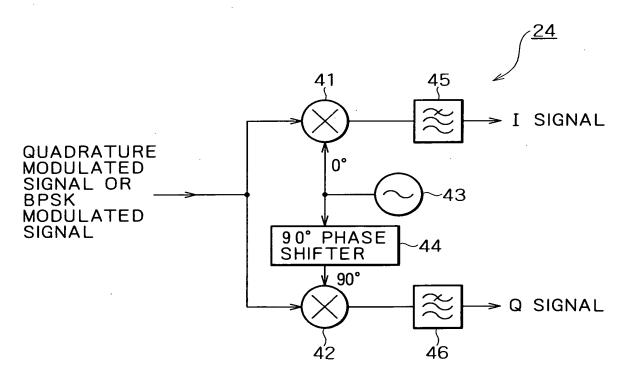


FIG.3

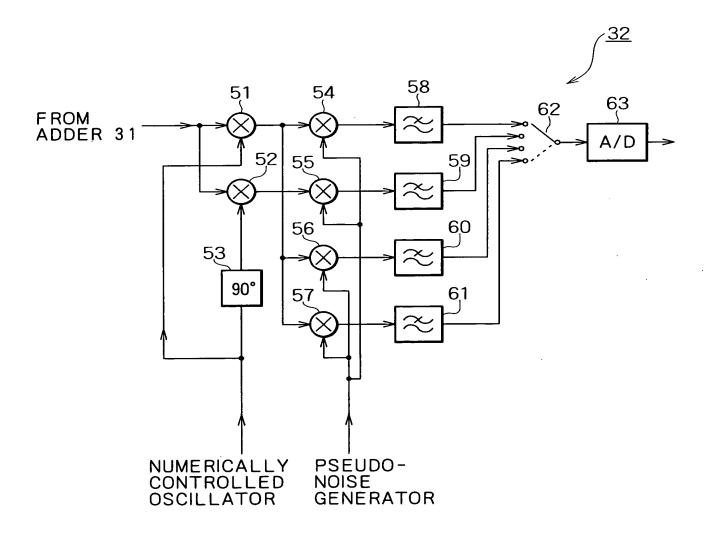
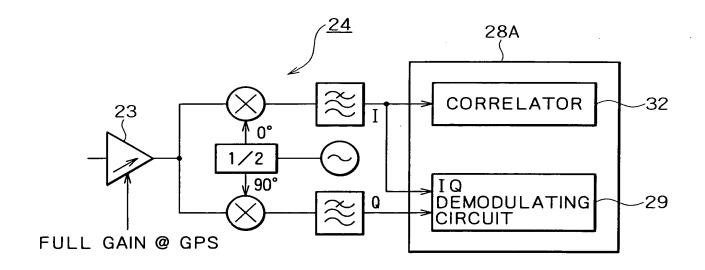


FIG.4



ARITHMETIC 28B GPS PROCESSING SYSTEM N+M GPS PROCESSING SYSTEM N GPS PROCESSING SYSTEM 1 GPS PROCESSING SYSTEM N+1 IQ DEMO-DULATING CIRCUIT F1G.5 0 }} <u>24</u> 。 06 / FULL GAIN @ GPS